

Table 3.38 Intel Celeron Variations

S-spec	Core Stepping	L2 Size	CPUID	Speed Core/Bus	Package	Notes (see footnotes)
SL2SY	dA0	0	0650h	266/66MHz	SEPP	
SL2YN	dA0	0	0650h	266/66MHz	SEPP	1
SL2YP	dA0	0	0650h	300/66MHz	SEPP	
SL2Z7	dA0	0	0650h	300/66MHz	SEPP	1
SL2TR	dA1	0	0651h	266/66MHz	SEPP	
SL2QG	dA1	0	0651h	266/66MHz	SEPP	1
SL2X8	dA1	0	0651h	300/66MHz	SEPP	
SL2Y2	dA1	0	0651h	300/66MHz	SEPP	1
SL2Y3	dB0	0	0652h	266/66MHz	SEPP	1
SL2Y4	dB0	0	0652h	300/66MHz	SEPP	1
SL2WM	mA0	128KB	0660h	300A/66MHz	SEPP	3
SL32A	mA0	128KB	0660h	300A/66MHz	SEPP	1
SL2WN	mA0	128KB	0660h	333/66MHz	SEPP	3
SL32B	mA0	128KB	0660h	333/66MHz	SEPP	1
SL376	mA0	128KB	0660h	366/66MHz	SEPP	
SL37Q	mA0	128KB	0660h	366/66MHz	SEPP	1
SL39Z	mA0	128KB	0660h	400/66MHz	SEPP	
SL37V	mA0	128KB	0660h	400/66MHz	SEPP	1
SL3BC	mA0	128KB	0660h	433/66MHz	SEPP	
SL35Q	mB0	128KB	0665h	300A/66MHz	PPGA	2
SL36A	mB0	128KB	0665h	300A/66MHz	PPGA	
SL35R	mB0	128KB	0665h	333/66MHz	PPGA	2
SL36B	mB0	128KB	0665h	333/66MHz	PPGA	
SL36C	mB0	128KB	0665h	366/66MHz	PPGA	
SL35S	mB0	128KB	0665h	366/66MHz	PPGA	2
SL3A2	mB0	128KB	0665h	400/66MHz	PPGA	
S-spec	Core Stepping	L2 Size	CPUID	Speed Core/Bus	Package	Notes (see footnotes)
SL37X	mB0	128KB	0665h	400/66MHz	PPGA	2
SL3BA	mB0	128KB	0665h	433/66MHz	PPGA	
SL3BS	mB0	128KB	0665h	433/66MHz	PPGA	2
SL3EH	mB0	128KB	0665h	466/66MHz	PPGA	
SL3FL	mB0	128KB	0665h	466/66MHz	PPGA	2

SEPP = Single Edge Processor Package

PPGA = Plastic Pin Grid Array

1. Boxed processor with an attached fan heat sink
2. Boxed processor with an unattached fan heat sink
3. Also available as a boxed processor with an attached fan heat sink