

Table 23.3 Mobile Pentium Processor Versions and Steppings

Type	Family	Model	Stepping	Mfg. Stepping	Speed	Spec. Number	Comments
0	5	2	1	B1	75-50	Q0601	TCP
0	5	2	2	B3	75-50	Q0606	TCP
0	5	2	2	B3	75-50	SX951	TCP
0/2	5	2	4	B5	75-50	Q0704	TCP
0	5	2	4	B5	75-50	SX975	TCP
0	5	2	5	C2	75-50	Q0725	TCP
0	5	2	5	C2	75-50	SK079	TCP
0	5	2	5	m A 1	75-50	Q0686	VRT, TCP
0	5	2	5	m A 1	75-50	Q0689	VRT
0	5	2	5	m A 1	90-60	Q0694	VRT, TCP
0	5	2	5	m A 1	90-60	Q0695	VRT
0	5	2	5	m A 1	75-50	SK089	VRT, TCP
0	5	2	5	m A 1	75-50	SK091	VRT
0	5	2	5	m A 1	90-60	SK090	VRT, TCP
0	5	2	5	m A 1	90-60	SK092	VRT
0	5	2	B	mcB1	100-66	Q0884	VRT, TCP
0	5	2	B	mcB1	120-60	Q0779	VRT, TCP
0	5	2	B	mcB1	120-60	Q0808	
0	5	2	B	mcB1	100-66	SY029	VRT, TCP
0	5	2	B	mcB1	120-60	SK113	VRT, TCP
0	5	2	B	mcB1	120-60	SK118	VRT, TCP
0	5	2	B	mcB1	120-60	SX999	
0	5	7	0	m A 4	75-50	Q0848	VRT, TCP
0	5	7	0	m A 4	75-50	Q0851	VRT
0	5	7	0	m A 4	90-60	Q0849	VRT, TCP
0	5	7	0	m A 4	90-60	Q0852	VRT
0	5	7	0	m A 4	100-66	Q0850	VRT, TCP
0	5	7	0	m A 4	100-66	Q0853	VRT
0	5	7	0	m A 4	75-50	SK119	VRT, TCP
0	5	7	0	m A 4	75-50	SK122	VRT
0	5	7	0	m A 4	90-60	SK120	VRT, TCP
0	5	7	0	m A 4	90-60	SK123	VRT
0	5	7	0	m A 4	100-66	SK121	VRT, TCP
0	5	7	0	m A 4	100-66	SK124	VRT
0	5	2	C	mcC0	100-66	Q0887	VRT, TCP
0	5	2	C	mcC0	120-60	Q0879	VRT, TCP
0	5	2	C	mcC0	120-60	Q0880	3.1v
0	5	2	C	mcC0	133-66	Q0881	VRT, TCP

Type	Family	Model	Stepping	Mfg. Stepping	Speed	Spec. Number	Comments
0	5	2	C	mCC0	133-66	Q0882	3.1v
0	5	2	C	mCC0	150-60	Q024	VRT, TCP
0	5	2	C	mCC0	150-60	Q0906	TCP, 3.1v
0	5	2	C	mCC0	150-60	Q040	VRT
0	5	2	C	mCC0	75-50	SY056	VRT/TCP
0	5	2	C	mCC0	100-66	SY020	VRT/TCP
0	5	2	C	mCC0	100-66	SY046	3.1v
0	5	2	C	mCC0	120-60	SY021	VRT, TCP
0	5	2	C	mCC0	120-60	SY027	3.1v
0	5	2	C	mCC0	120-60	SY030	
0	5	2	C	mCC0	133-66	SY019	VRT, TCP
0	5	2	C	mCC0	133-66	SY028	3.1v
0	5	2	C	mCC0	150-60	SY061	VRT, TCP
0	5	2	C	mCC0	150-60	SY043	TCP, 3.1v
0	5	2	C	mCC0	150-60	SY058	VRT
0	5	2	6	E0	75-50	Q0846	TCP
0	5	2	6	E0	75-50	SY009	TCP

Table 23.4 Mobile Pentium MMX Processor Versions and Steppings

Type	Family	Model	Stepping	Mfg. Stepping	Speed	Spec. Number	Comments
0	5	4	4	mxA3	150-60	Q016	ES, TCP, 2.285V
0	5	4	4	mxA3	150-60	Q061	ES, PPGA, 2.285V
0	5	4	4	mxA3	166-66	Q017	ES, TCP, 2.285V
0	5	4	4	mxA3	166-66	Q062	ES, PPGA, 2.285V
0	5	4	4	mxA3	150-60	SL22G	TCP, 2.285V
0	5	4	4	mxA3	150-60	SL246	PPGA, 2.285V
0	5	4	4	mxA3	166-66	SL22F	TCP, 2.285V
0	5	4	4	mxA3	166-66	SL23Z	PPGA, 2.285V
0	5	4	3	mxB1	120-60	Q230	ES, TCP, 2.2v
0	5	4	3	mxB1	133-66	Q130	ES, TCP, 2.285V
0	5	4	3	mxB1	133-66	Q129	ES, PPGA, 2.285V
0	5	4	3	mxB1	150-60	Q116	ES, TCP, 2.285V
0	5	4	3	mxB1	150-60	Q128	ES, PPGA, 2.285V
0	5	4	3	mxB1	166-66	Q115	ES, TCP, 2.285V
0	5	4	3	mxB1	166-66	Q127	ES, PPGA, 2.285V
0	5	4	3	mxB1	133-66	SL27D	TCP, 2.285V
0	5	4	3	mxB1	133-66	SL27C	PPGA, 2.285V
0	5	4	3	mxB1	150-60	SL26U	TCP, 2.285V
0	5	4	3	mxB1	150-60	SL27B	PPGA, 2.285V
0	5	4	3	mxB1	166-66	SL26T	TCP, 2.285V
0	5	4	3	mxB1	166-66	SL27A	PPGA, 2.285V
0	5	8	1	myA0	166-66	Q255	TCP, 1.8v
0	5	8	1	myA0	200-66	Q146	TCP, 1.8v
0	5	8	1	myA0	233-66	Q147	TCP, 1.8v
0	5	8	1	myA0	200-66	SL28P	TCP, 1.8v
0	5	8	1	myA0	233-66	SL28Q	TCP, 1.8v
0	5	8	1	myA0	266-66	Q250	TCP, 2.0v

ES = Engineering Sample. These chips were not sold through normal channels, but were designed for development and testing purposes.

STP = The cB1 stepping is logically equivalent to the C2 step, but on a different manufacturing process. The mcB1 step is logically equivalent to the cB1 step (except it does not support DP, APIC, or FRC). The mcB1, mA1, mA4, and mcC0 steps also use Intel's VRT (voltage reduction technology), and are available in the TCP or SPGA package, primarily to support mobile applications. The mxA3 is logically equivalent to the xA3 stepping (except it does not support DP or APIC).

TCP = Tape Carrier Package

PPGA = Plastic PGA

VRT = Voltage Reduction Technology

2.285v = This is a mobile Pentium processor with MMX technology with a core operating voltage of 2.285v–2.665v.

1.8v = This is a mobile Pentium processor with MMX technology with a core operating voltage of 1.665v–1.935v and an I/O operating voltage of 2.375v–2.625v.

2.2v = This is a mobile Pentium processor with MMX technology with a core operating voltage of 2.10v–2.34v.

2.0v = This is a mobile Pentium processor with MMX technology with a core operating voltage of 1.850v–2.150v and an I/O operating voltage of 2.375v–2.625v.

Table 23.5 Mobile Pentium II Processor Steppings

Family	Model	Core Stepping	L2 Cache Size (KB)	L2 Cache Speed	S-Spec	Core/Bus	Notes
6	5	mdA0	512	1/2 core	SL2KH	233/66	MC, 1.7v
6	5	mdA0	512	1/2 core	SL2KJ	266/66	MC, 1.7v
6	5	mdmA0	512	1/2 core	PMD233	233/66	MMO, 1.7v
6	5	mdmA0	512	1/2 core	PMD266	266/66	MMO, 1.7v
6	5	mdB0	512	1/2 core	SL2RS	300/66	MC, 1.6v1
6	5	mdB0	512	1/2 core	SL2RR	266/66	MC, 1.6v1
6	5	mdB0	512	1/2 core	SL2RQ	233/66	MC, 1.6v1
6	5	mdxA0	256	core	SL32M	266/66	MC, 1.6v1
6	5	mdxA0	256	core	SL32N	300/66	MC, 1.6v1
6	5	mdxA0	256	core	SL32P	333/66	MC, 1.6v1
6	5	mdxA0	256	core	SL36Z	366/66	MC, 1.6v1
6	5	mdbA0	256	core	SL32Q	266/66	BG, 1.6v2
6	5	mdbA0	256	core	SL32R	300/66	BG, 1.6v2
6	5	mdbA0	256	core	SL32S	333/66	BG, 1.6v2
6	5	mdbA0	256	core	SL3AG	366/66	BG, 1.6v2
6	5	mdbA0	256	core	SL3DR	266/66	BG, 1.5v
6	5	mdbA0	256	core	SL3HH	266/66	PG, 1.6v2
6	5	mdbA0	256	core	SL3HJ	300/66	PG, 1.6v2
6	5	mdbA0	256	core	SL3HK	333/66	PG, 1.6v2
6	5	mdbA0	256	core	SL3HL	366/66	PG, 1.6v2

1.7v = Core processor voltage is 1.7v for these mobile Pentium processors

1.6v1 = Core processor voltage is 1.6v +/- 120mV for these processors

1.6v2 = Core processor voltage is 1.6v +/- 135mV for these processors

1.5v = Core processor voltage is 1.6v +/- 135mV for this processor

MC = Mobile Pentium II processor mounted in a mini-cartridge

MMO = Mobile Pentium II processor mounted in a mobile module including the 440BX North Bridge

BG = Mobile Pentium II processor in BGA packaging for low-profile surface mounting

PG = Mobile Pentium II processor in a (upsilon symbol) PGA1 package

Table 23.6 Mobile Celeron Processor Steppings

Family	Model	Core Stepping	L2 Cache (KB)	S-Spec	Speed Core/Bus	Notes
6	6	mcbA0	128	SL23Y	266/66	B,1
6	6	mcbA0	128	SL3AH	300/66	B,1
6	6	mcbA0	128	SL3C8	333/66	B,1
6	6	mcbA0	128	SL3DQ	266/66	B,2
6	6	mcbA0	128	SL3GQ	400/66	B,1
6	6	mcpA0	128	SL3HM	266/66	P,1
6	6	mcpA0	128	SL3HN	300/66	P,1
6	6	mcpA0	128	SL3HP	333/66	P,1
6	6	mcpA0	128	SL3HQ	366/66	P,1
6	6	mcpA0	128	SL3GR	400/66	P,1

B = In a BGA package

P = In a (upsilon symbol) PGA package

1 = Runs at a core voltage of 1.6v

2 = Runs at a core voltage of 1.5v

Table 23.7 Mobile AMD K6 Processors

CPU Model	Core Voltage	Clock Speeds	Notes
Mobile K6	2.1v	233, 266	66
Mobile K6	2.2v	300	66
Mobile K6-2	1.8v	266, 300, 333, 350, 366, 380	66, 3DN
Mobile K6-III	2.2v	350, 366, 380	100, 256KL2, 3DN

66 = Has a 66MHz bus speed

100 = Has a 100MHz bus speed

256KL2 = Contains a 256KB integrated L2 cache running at full processor speed and supports L3 cache on the motherboard

3DN = Supports 3Dnow! multimedia enhancements